

Spatial Energy Research

Stiffler Scientific

Dr. Ronald Stiffler

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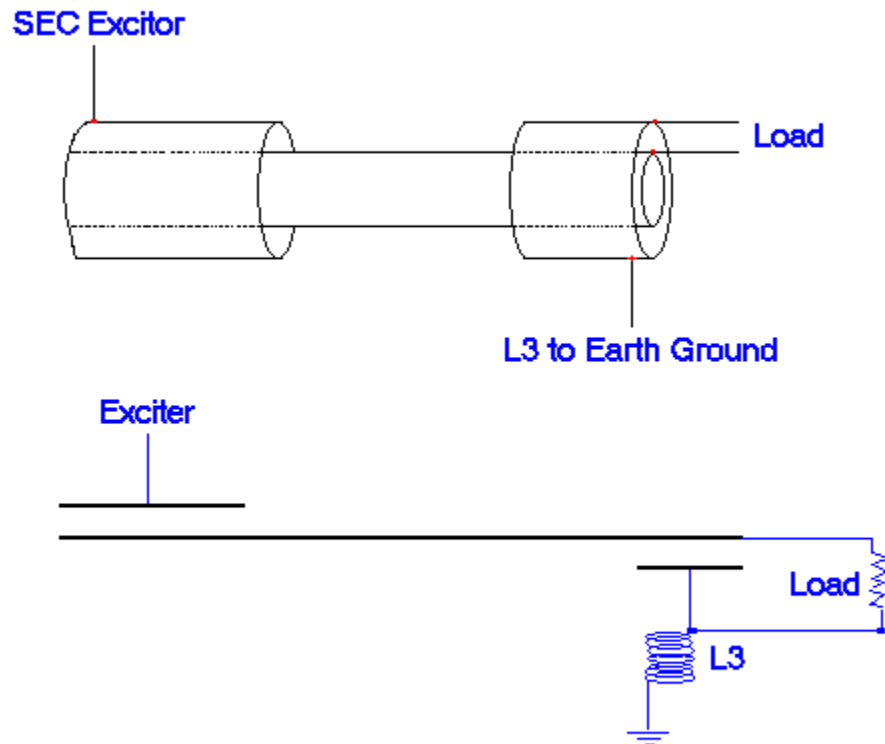
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The 'Spatial Gate' is the AC (Alternating Current) equivalent of the AV Plug used in many SEC Exciter Circuits.

Where DC (Direct Current) is not required, the 'Spatial Gate' produces high voltage AC which can be as much as 30% greater than what can be obtained from a single AV Plug arrangement.

Fig: 1 & 2

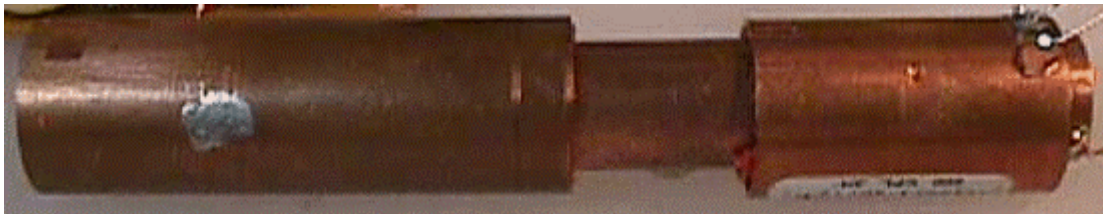
Spatial Gate



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The following picture is a prototype 'Spatial Gate' and in this case the gate is constructed from Copper Tubing.

Fig: 3



The following schematic if of a basic SEC Exciter driving a 'Spatial Gate'. The circuit fully illustrates the simplicity of the electronics required to obtain a Energy Interface.

Fig:4

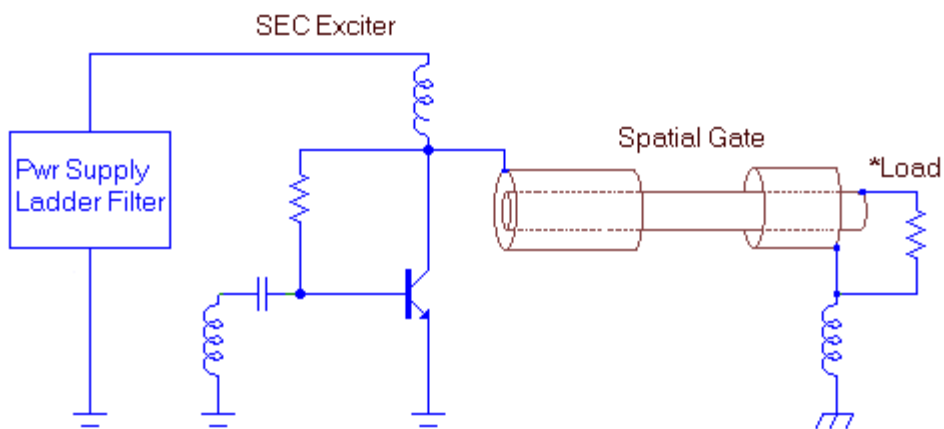
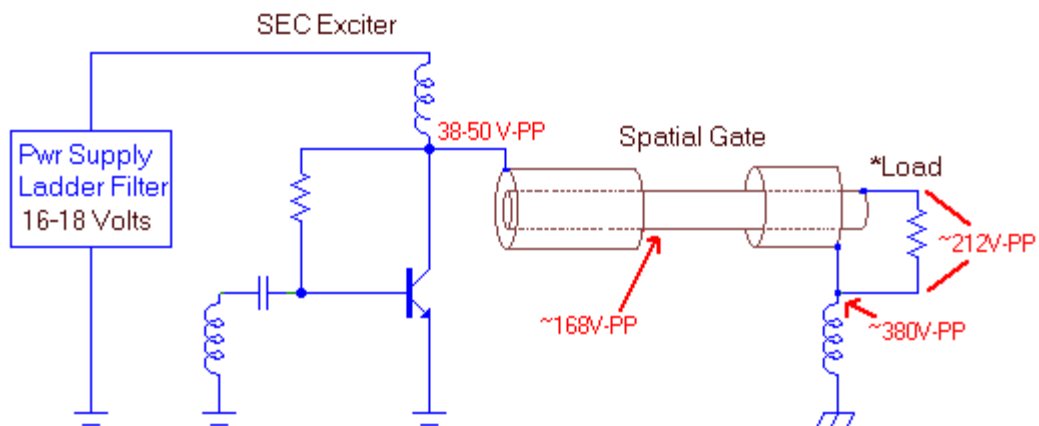


Fig: 5



The following two video's show a 'Spatial Gate' in operation.

[Cool White Power #4 & Spatial Gate](#)

and

[Spatial Coherence & Spatial Gate #1](#)

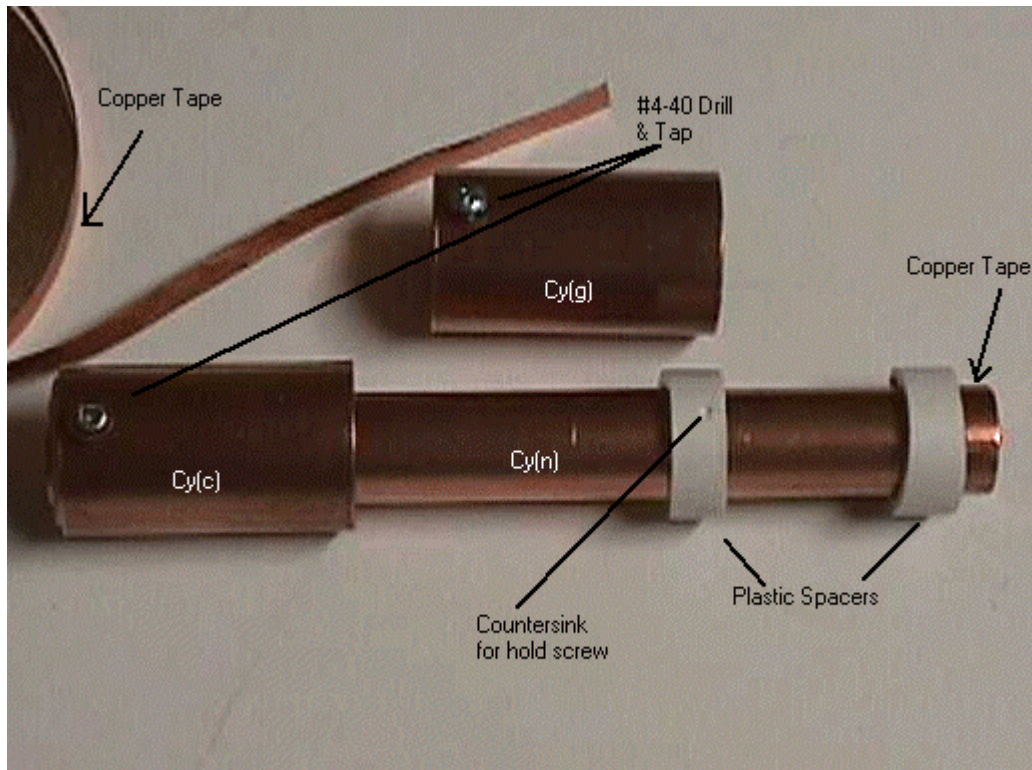
Construction of a 'Spatial Gate' can be easy or hard, yet all materials can be purchased from a Home Improvement Store and a Crafts Supply Store.

Standard sizes of copper pipe, couplings and plastic pipe used for spacers are the

basics needed to make a 'Gate'.

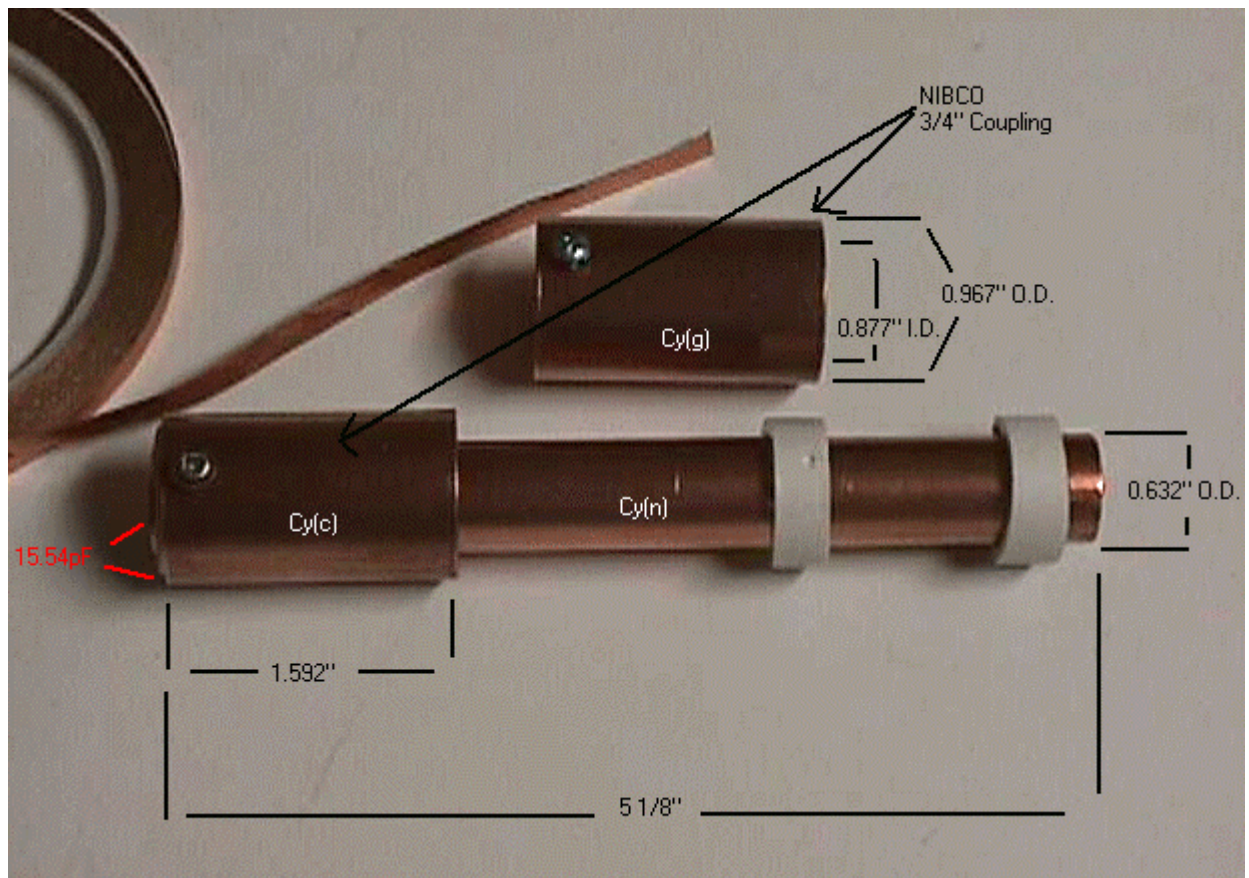
The following picture shows the 15pF Balanced Gate partially assembled

Fig: 6



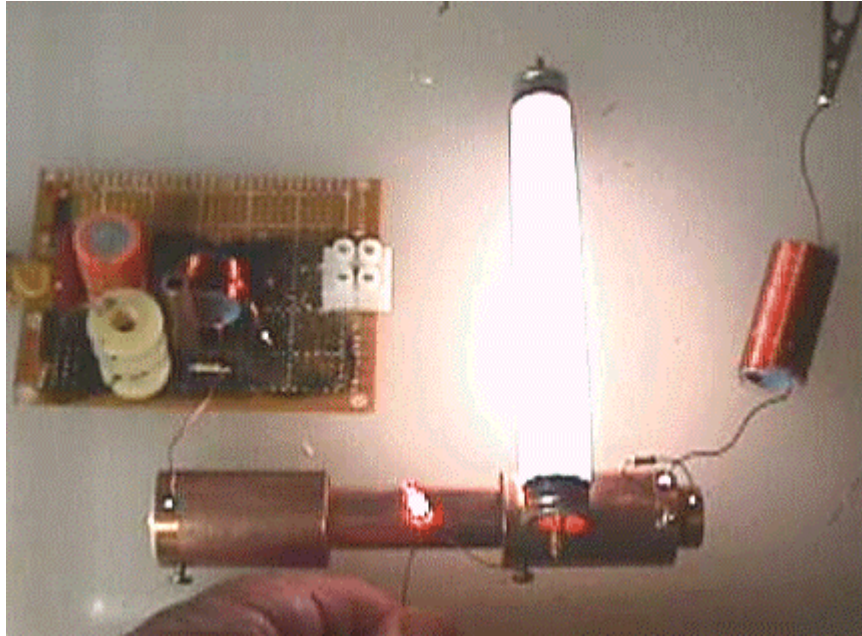
Another view with measurements of the 15pF Balanced Gate.

Fig: 7



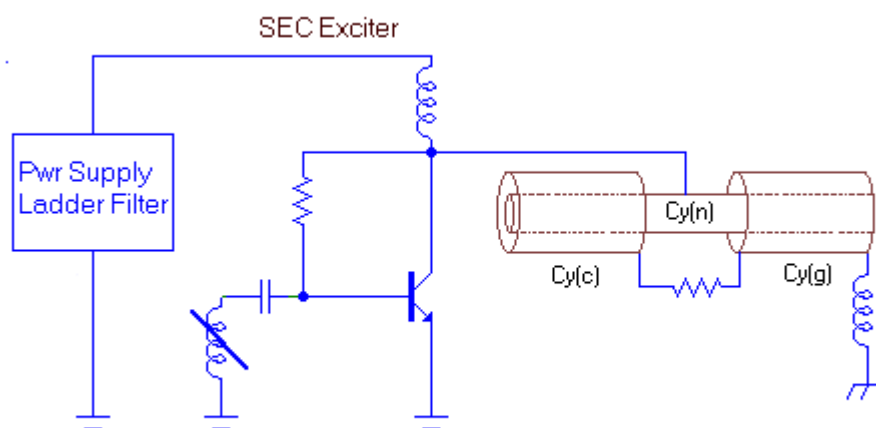
The following picture is of a 15 pF Balanced 'Spatial Gate' (See Fig: 6 & 7) in a running mode.

Fig: 8



A variation of how the resistive load can be connected in a 15pF Balanced Gate. In this variation the load is connected between $Cy(c)$ and $Cy(g)$ while the Exciter Collector is connected to the center of $Cy(n)$.

Fig: 9



The following picture is of a 15pF Balanced Gate running the incandescent connected between $Cy(c)$ and $Cy(g)$.

Fig: 10

